



processor, each instruction comprising an identification segment and an instruction segment, the method comprising:

- using an assembler to assemble instructions into different execution packages;
- using the assembler to reorder the instructions by separating identification segments from instruction segments, grouping all identification segments of the execution package together, and grouping all instruction segments of the execution package together;
- using the processor to decode identification segments of the instructions at the same time; and
- adding a length of each identification segment together to calculate a total length of the execution package.

[c11] 11.The method of claim 10 wherein the instructions are variable length instructions.